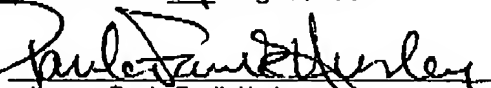


Application No. 10/632,553
Attorney Docket: CPAC 1017-6 D4**CERTIFICATE OF FACSIMILE TRANSMISSION**

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| In re application of: Marcos Kamezos | Attorney Docket No.: CPAC 1017-6 D4 |
| Application No.: 10/632,553 | Examiner: Unassigned |
| Filed: 02 August 2003 | Group: 2812 |
| Title: Semiconductor Multi-Package Module Having Package Stacked Over Die-Down Flip Chip Ball Grid Array Package And Having Wire Bond Interconnect Between Stacked Packages | Confirmation No.: 2573 |
| | Customer No. 22470 |

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P.O. Box 1450
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Enclosed with this statement is a Form PTO/SB/08A. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. However, since this application was filed after June 30, 2003, copies of U. S. Patents and U. S. Patent Application Publications are not enclosed. (See O. G. Notice of August 5, 2003.)

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Application No. 10/632,553
Attorney Docket: CPAC 1017-6 D4

thereof, is within the possession, custody or control of, or is readily available to any individual designated in 1.56(c), a copy of the translation accompanies this statement, 37 C.F.R. 1.98(a)(3)(ii).

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed before the mailing date of the first Office Action on the merits.

Fee Authorization. The Commissioner is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (CPAC 1017-6 D4). A duplicate copy of this authorization is enclosed.

Respectfully submitted,
Haynes Beffel & Wolfeld LLP

Date: 04 August 2004

By:

 Reg. No. 33,407
Bill Kennedy, Reg. No. 33,407

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Application No. 10/632,553
Attorney Docket: CPAC 1017-6 D4

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| In re application of: Marcos Karnezos | Attorney Docket No.: CPAC 1017-6 D4 |
| Application No.: 10/632,553 | Examiner: Unassigned |
| Filed: 02 August 2003 | Group: 2812 |
| Title: Semiconductor Multi-Package Module Having Package Stacked Over Die-Down Flip Chip Ball Grid Array Package And Having Wire Bond Interconnect Between Stacked Packages | Confirmation No.: 2573 |
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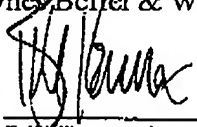
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| | | | Application Number | 10/632,553 | |
| | | | Filing Date | 08/02/03 | |
| | | | First Named Inventor | Kamezos | |
| | | | Group Art Unit | 2812 | |
| | | | Examiner Name | Unassigned | |
| Sheet | 1 | of | 1 | Attorney Docket Number | CPAC 1017-6 D4 |

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